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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10015913	FILING DATE 01/09/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER G. E. Brennan
<b>**APPLICANTS:</b> Singh Rana; Ingersoll Paul;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CIP OF 09/997,145 11/29/2001 T THOMAS					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO SC11448TP P01	
Verified and Acknowledged Examiners's initials <i>[Signature]</i>					
TITLE : Semiconductor device structure and method for forming					

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

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<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	
		Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		PREPARED FOR ISSUE	
		Applicati n Examiner	
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